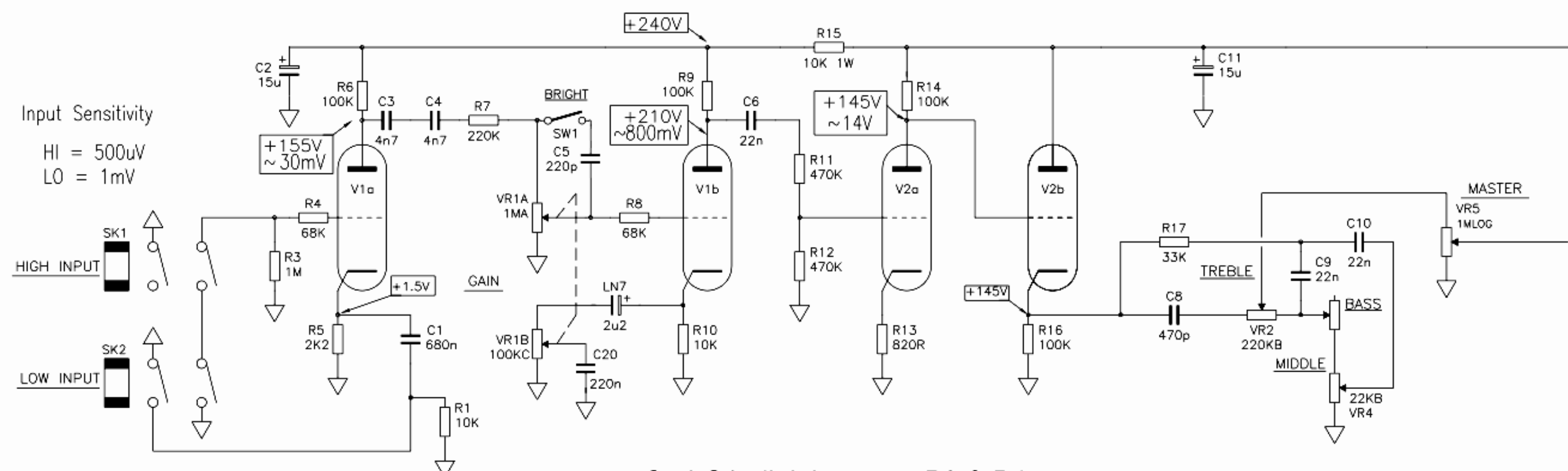
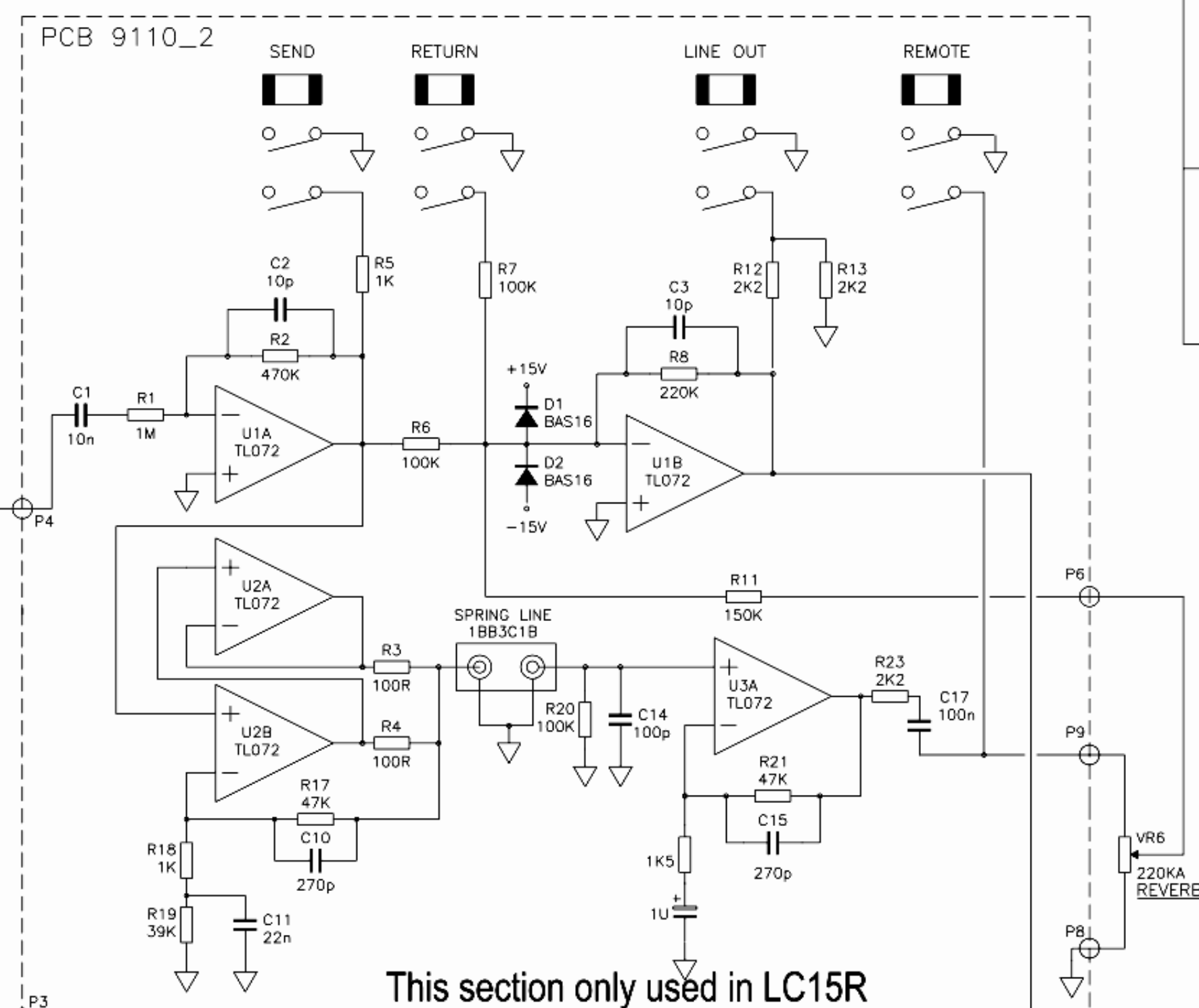
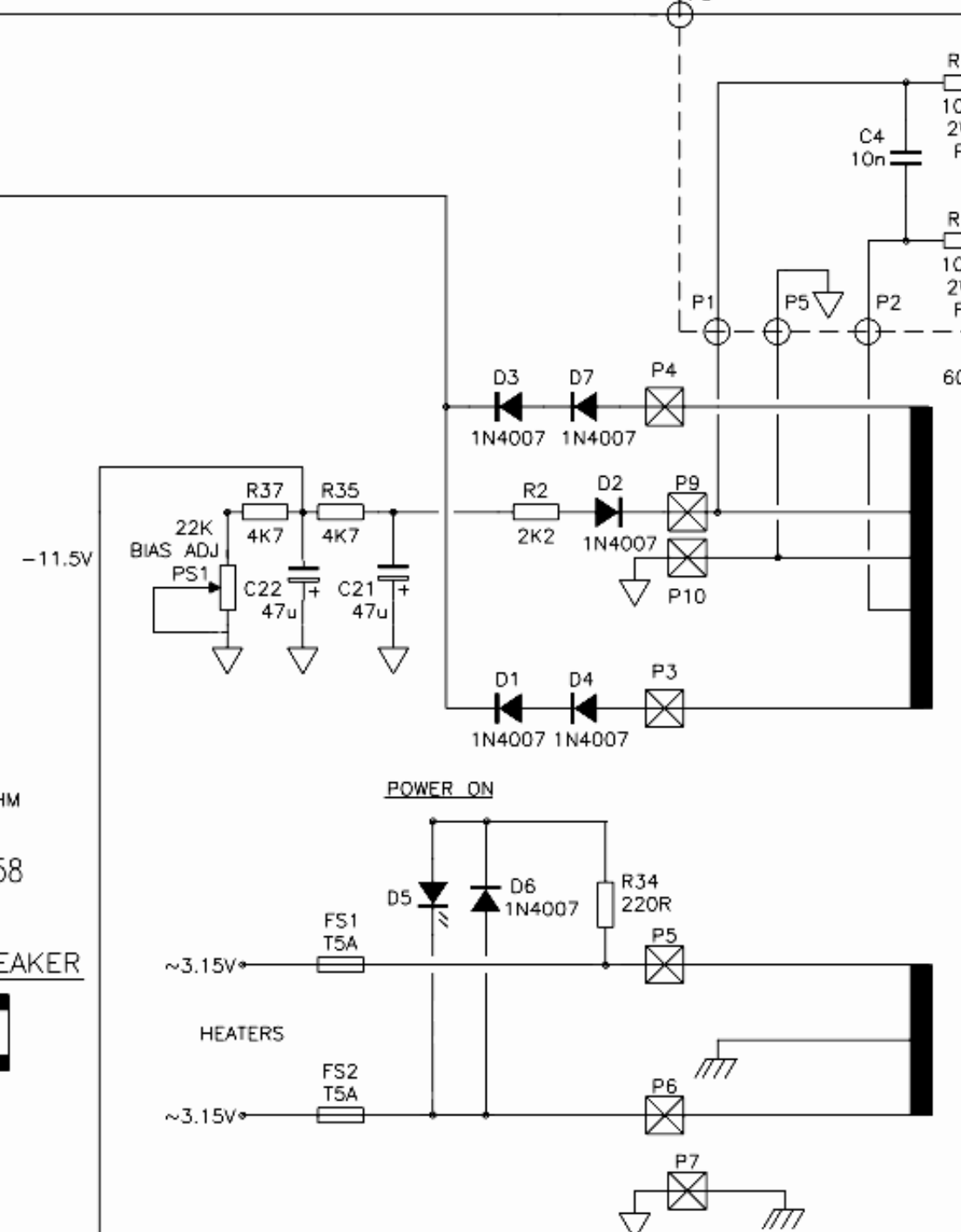
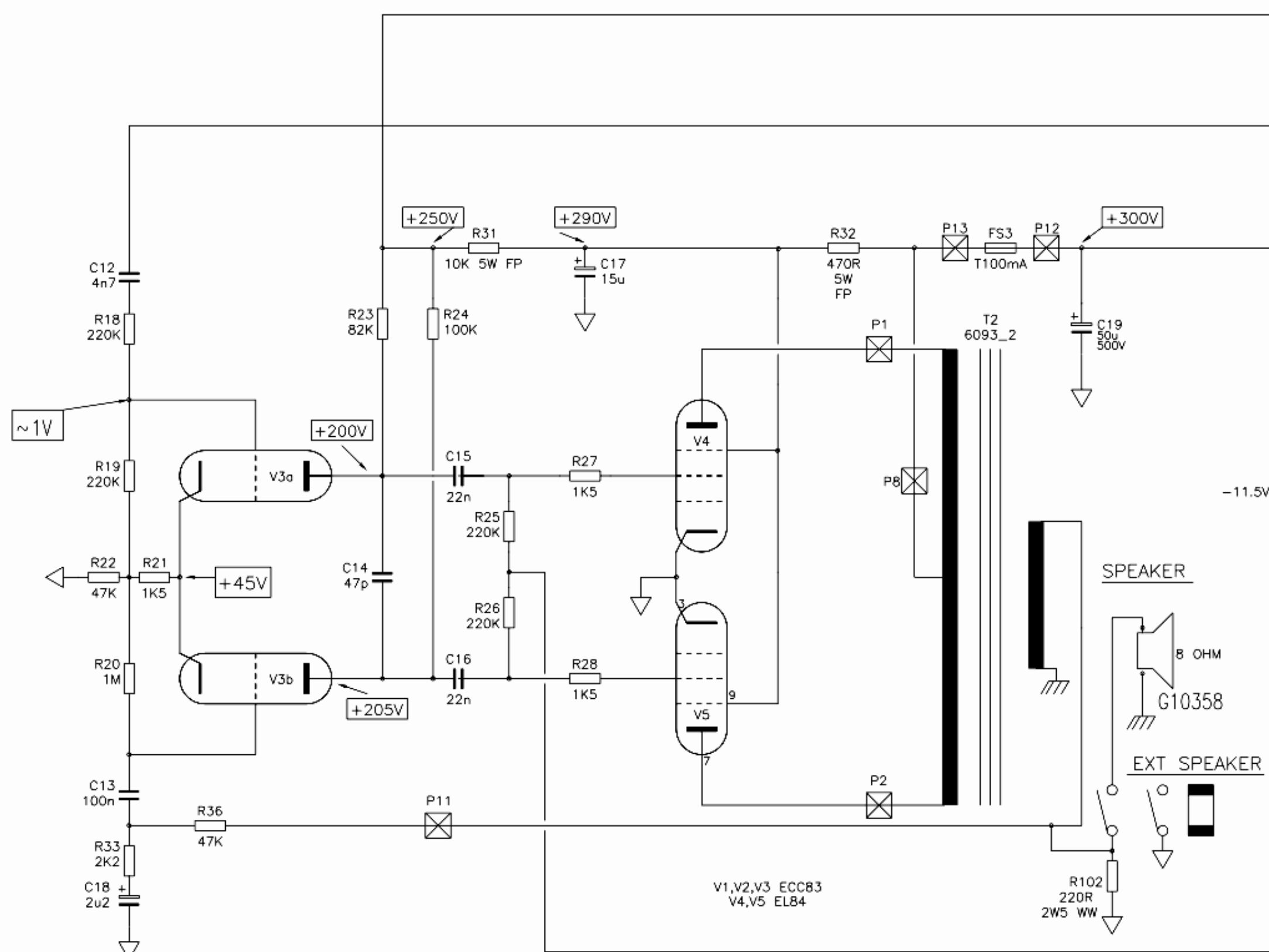


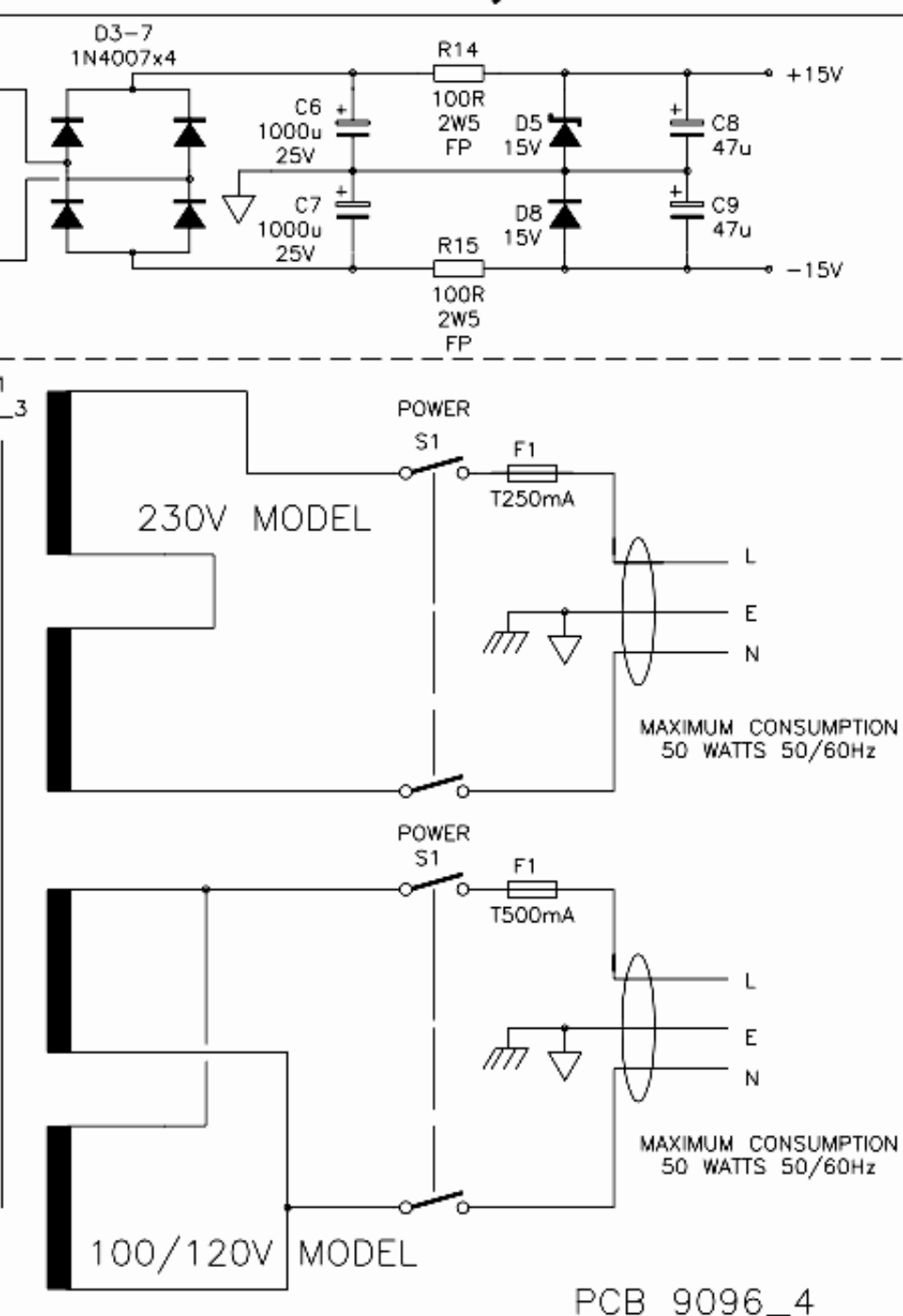
Input sensitivity = Gain & Eq up full, Master set to No.5



On LC15 link between P3 & P4
and omit all items in boxed area.



This section only used in LC15R



ISSUE	DESCRIPTION	APPD.	DATE
	Gain pot from A1M/A100K to A1M/C100K dual	R.J.F	4/12/03
	LN7 changed from 0r0 to 2u2	R.J.F	25/9/00

LANEY

LC15 / LC15R
CIRCUIT
PART No

DRAWN	TRACED	CHECKED	APPROVED	DATE
D.E.H.				19/02/97
DRAWING No.		2557		

DWG.No. 2557